



IN THE UNITED STATES

PATENT AND TRADEMARK OFFICE

~~TRADE~~ PATENT APPLICATION

Inventor(S) Yih-Feng Chyan Case: 15-6-9

Serial No. 09/648164 Group Art Unit: 2826

Filing Date August 25, 2000 Examiner: Dickey

Title: Architecture For Circuit Connection Of A Vertical Transistor

**ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D. C. 20231**

RECEIVED

MAR 12 2003

SIR:

PETITION FOR EXTENSION OF TIME

Applicant(s) petition(s) the Commissioner of Patents and Trademarks to extend the time for response to the action dated 11/05/03 for 1 (one) month from 2/5/04 to 3/5/04.

Please charge Agere Systems, Inc. **Deposit Account No. 501735** in the amount of \$110.00 to cover the cost of the extension. Any deficiency or overpayment of the required fee should be charged or credited to **Deposit Account No. 501735** as required to correct the error. Duplicate copies of this petition are enclosed.

Respectfully Submitted,

By: Ferdinand M. Romano
Ferdinand M. Romano
Reg. 32,752
Attorney for Applicant

Date: March 5, 2004

03/10/2004 SDFNB0B1 00000117 501735 09648164

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